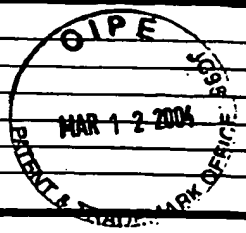


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Substitute for form 1449B/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> Date Submitted <b>MAR 12 2004</b> (use as many sheets as necessary)				<b>Complete if Known</b>	
Application Number		10/676,264			
Filing Date		10/02/2003			
First Named Inventor		Hitoshi TSUNO			
Group Art Unit		2811			
Examiner Name		Unassigned			
Attorney Docket Number		016907-1577			
Sheet	1	of	1		



U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
<i>RA</i>	B1	K. Ota et al., "Double Offset Implantation Technique for High Performance 80nm CMOSFET with Low Gate Leakage Current", SEMI Forum 2002, ULSI Technology Seminar, Section 4, pages 42-47.	

Examiner Signature <i>[Signature]</i>	Date Considered <i>11/19/04</i>
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

<sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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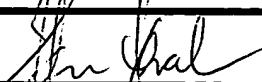
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Substitute for form 1449B/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  Date Submitted: October 2, 2003  (use as many sheets as necessary)				Application Number	Unassigned
				Filing Date	10/02/2003
				First Named Inventor	Hitoshi TSUNO
				Group Art Unit	Unassigned
				Examiner Name	Unassigned
Sheet	1	of	1	Attorney Docket Number	016907-1577

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
PA	A1	6,333,540	B1	SHIOZAWA ET AL.	12-25-2001	
	A2	6,387,743	B1	SHIOZAWA ET AL.	05-14-2002	
	A3	5,994,743		MASUOKA	11-30-1999	

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
PA	A4	JP	11-220038	A	LSI LOGIC CORP.	08-10-1999		Abstract
	A5	JP	2001-308318	A	MITSUBISHI ELECTRIC CORP.	11-02-2001		Abstract
✓	A6	JP	10-223772	A	NIPPON ELECTRIC CO.	08-21-1998		Abstract
	A7	EP	0 932 197	A1	LSI LOGIC CORPORATION	07-28-1999		

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